FEB 2 3 2004 (S)

Application Serial No 09/834,660
Filing Date April 12, 2001
Inventor Luan C. Tran
Assignee Micron Technology, Inc.
Group Art Unit
Examiner Laura Schillinger
Attorney's Docket No MI22-1637
Title: Semiconductor Processing Methods Of Forming Transistors, Semiconductor
Processing Methods Of Forming Dynamic Random Access Memory
Circuitry, and Related Integrated Circuitry

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

References - See Attached Form PTO-1449

The Examiner's attention is directed to the references which are listed on the attached Form PTO-1449, copies of which are attached. No admission is made regarding whether all the submitted references are prior art.

Citation of the referenced art is respectfully requested.

Respectfully submitted,

Dated: 20,2002

By:

Frederick M. Fliegel, Ph.D.

Reg. No. 36,138

EL979954655

Sheet 1 of 3



U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE

ATTY. DOCKET NO. SERIAL NO. 09/834.660

LIST OF ART CITED BY APPLICANT (Use several sheets if necessary)

APPLICANT Luan C. Tran

			(Use several sheets	if necessary)			Luan C. Tran				
							FILING DATE April 12, 2001		GRC 2813	UP	
					U.S. PATI	ENT DOCUMENTS					
*Examiner Initial			Document Number	Date		Name		Class	Subclass	. Filin	g Date ropriate
	A	`	5,926,723	07/20/99	Wang						
	A	В	6,420,749 B1	07/16/02	Divakaru	ni et al.					
	A	c ,	6,177,324 B1	01/23/01	Song et a	al.					
	Ai	D ,	5,429,081 B1	08/06/02	Doong et	t al.					
	AE		5,121,651	09/19/00	Furukawa	et al.					
	AF		5,492,858	02/20/96	Bose et a	al.					
	A	٠ ا	6,323,082 B1	11/27/01	Furukawa	et al.					
	AI	Η ,	5,156,620	12/05/00	Puchner	et al.					
- · - · · · · · · · · · · · · · · · · ·	А	.1 .	6,048,765	04/11/00	Wu						
	A	, ,	6,351,019 B1	02/26/02	DeBrosse	et al.					
	Al	× i	5,153,467	11/28/00	Wu						
					FOREIGN P	ATENT DOCUMENT	S			· · · · · · · · · · · · · · · · · · ·	
		Doc Nur	ument iber	Date	1	Country		Class	Subclass	Tran	slation
	 									Yes	No
	AL									<u> </u>	
- ,	AM				 						+
	AN						-				
	AO AP										
	1 76			OTHER REFERE	ENCES (includin	ig Author, Title, Date,	Pertinent Pages, Etc.)	-	.		<u> </u>
	AQ		Wolf et al., "Silicon	Processing for the	VLSI Era" • V	Volume ! - Process To	echnology - Second Edition	on, Ø 2000, 8	5 pages.		
	AR										
	-										
	AS									 	
- FV LANCE	<u> </u>					DATE COMMENT					
EXAMINER						DATE CONSIDERE					

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

S:\MI22\1637\ID8.wpd A270208211132N

Sheet 2 of 3



Form PTO-1449

LIST OF ART CITED BY APPLICANT (Use several sheets if necessary)

U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE

ATTY, DOCKET NO. M122-1637 SERIAL NO. 09/834,660

APPLICANT Luan C. Tran

FILING DATE

GROUP

				U.S. PATENT DOCUMENTS	·	•	,	7
Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing If Appro	Date opriate
	AA	6,236,079 B1	05/22/01	Nitayama et al.				
	AB	6,294,422 B1	09/25/01	Sunouchi et al.				
	AC	6,133,055	10/17/00	Yeh				
	AD	6,353,242 B1	03/05/02	Watanabe et al.				
	AE	6,037,671	03/14/00	Kepler et al.				
	AF	6.028,784	02/22/00	Mori et al.				
	AG	6,025,623	02/15/00	Sunouchi et al.				
	АН	6,261,883 B1	07/17/01	Koubuchi e t al.				
	AI	6,403,484 B1	06/11/02	Lim et al.				
	Ę,	5,994,198	11/30/99	Hsu et al.				
	AK	6,060,394	05/09/00	Wu				
				FOREIGN PATENT DOCUMENTS		r		
		Document Number	Date	Country	Class	Subclass	Trans Yes	lation No
	AL						163	
	AM							
	AN							
	AO							
	ΑP					l		L
			OTHER REFERE	NCES (including Author, Title, Date, Pertinent Pages, Etc.)				
	AQ							
	AR							
	AS							
EXAMINER	L			DATE CONSIDERED				

FEB 2 3 2004 65
MADEMARK

Sheet 3 of 3

U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE

LIST OF ART CITED BY APPLICANT
(Use several sheets if necessary)

APPLICANT
Luan C. Tran

FILING DATE
April 12, 2001

2813

				April 12, 2001		2813		
				U.S. PATENT DOCUMENTS			-	
*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing If Appro	Date opriate
	AA	6,312,997 B1	11/06/01	Tran				
	АВ	6,411,555 B1	06/25/02	Tran				
	AC	5,892,707	04/06/99	Noble				
	AD	6,171,929 B1	01/09/01	Yang et al.				
	AE	6,391,756 B1	05/21/02	Pan et al.				
	AF	6,423,620 B2	07/23/02	Pan et al.				
	AG	6,297,129 B2	10/02/01	Tran et al.				
	АН	5,447,884	09/05/95	Fahey et al.				
······································	AI	5,851,900	12/22/98	Chu et al.				
<u> </u>	AJ							
	AK							<u> </u>
	,	r		FOREIGN PATENT DOCUMENTS		Т		
		Document Number	Date	FOREIGN PATENT DOCUMENTS Country	Class	Subclass		lation No
	AL	Document Number	Date		Class	Subclass	Trans Yes	lation No
	AL AM	Document Number	Date		Class	Subclass		· · · · · ·
		Document Number	Date		Class	Subclass		
	AM	Document Number	Date		Class	Subclass		
	AM AN	Document Number	Date		Class	Subclass		
	AM AN AO	Document Number			Class	Subclass		
	AM AN AO	Document Number		Country	Class	Subclass		
	AM AN AO AP	Document Number		Country	Class	Subclass		· · · · · ·
	AM AN AO AP	Document Number		Country	Class	Subclass		
	AM AN AO AP	Document Number		Country	Class	Subclass		· · · · · ·
	AM AN AO AP	Document Number		Country	Class	Subclass		
	AM AN AO AP AQ AR	Document Number	OTHER REFERE	Country	Class	Subclass		

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.